

Proposed change to LPS

- change number of clks for PHY to resume driving Control/Data
 - drive 0's: from < 3 clocks to ≤ 4 clocks
 - resume ops: from 3 clocks to ≥ 6 clocks
- change number of clks for LINK to drive Control/Data to 0's
 - from < 3 clocks to ≤ 4 clocks

Proposed change to LPS (con't)

- change resume normal operation:
 - if internal PHY state is idle,
drive idle on PHY-LINK
 - if internal PHY state not idle,
drive “data-on” (Ctl = *receive*, D = 'hF) until
internal state is idle, then switch to normal
behavior (idle)

PHY/LINK interface reset via LPS

